

PATENT NUMBER

# U.S. UTILITY Patent Application

(45) O.I.P.E.

**PATENT DATE**

**SCANNED**

## Q.A

APPLICATION NO. 09/800039	CONT/PRIOR 0	CLASS <del>426</del> 257	SUBCLASS 371	ART UNIT 2822	EXAMINER Prenter
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## APPLICANTS

**FILE**

Pervaz: Bagarwala  
 Mehdi Zamanian  
 Rayi Sundarasan

CMOS integrated circuit device with LDD n-channel transistor and non-LDD p-channel transistor

PTO-2040  
12/99

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION											

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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